PATENT ABSTRACTS OF JAPAN

(11)Publication number

08-186249

(43)Date of publication of application: 16.07.1996

(51)Int.CI.

H01L 29/778 H01L 21/338 H01L 29/812 H01L 21/8238 H01L 27/092 H01L 27/095

(21)Application number: **06-327156**

(71)Applicant:

FUJITSU LTD

(22)Date of filing:

28.12.1994

(72)Inventor:

AWANO YUJI

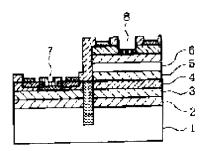
MAEDA TAKESHI

(54) SEMICONDUCTOR DEVICE AND ITS MANUFACTURE

(57) Abstract:

PURPOSE: To obtain a high-performance complementary semiconductor circuit device having a higher-speed characteristic than a silicon complementary circuit device, by simple manufacturing processes and with good reproducibility.

CONSTITUTION: A grated Si1-xGex layer 2 having an increasing Ge composition, an active layer 3 composed mainly of germanium, and a grated Sil-vGev layer 4 having a reducing Ge composition are provided on a silicon germanium substrate 1, and in its one part region a p-type transistor 7 having a silicon germanium active layer 3 as a channel layer is formed. Along with it, in an adjacent region an n-type transistor 8 is formed in a III-V compound semiconductor active layer 6 provided through the medium of a high-resistance III-V compound semiconductor layer 5



LEGAL STATUS

[Date of request for examination]

25.12.2001

[Date of sending the examiner's decision of rejection]

Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection|

[Date of extinction of right]

Copyright (C): 1998,2000 Japan Patent Office